ENGR 354 **Digital Logic** **Autumn 2018**

TEXT: MIT Computational Structures Notes

Date	Topic	Assignments due
M Sep 24	Basics of Information	
W 26	"	
F 28	"	
M Oct 1	The Digital Abstraction	
W 3	"	
F 5	"	
M 8	CMOS Technology	
W 10	"	
F 12	"	
M 15	Combinational Logic	Assignment 1
W 17	SERVICE DAY	
F 19	Combinational Logic	
M 22	Sequential Logic	Assignment 2
W 24	"	
F 26	"	
M 29	Finite State Machines	Assignment 3
W 31	"	
F 2	"	
M 5	Performance Measures	Assignment 4
W 7	"	
F 9	"	
M 12	Design Tradeoffs	
W 14	"	
F 16	"	Exam 1
S-S 18-25	THANKSGIVING BREAK!	
M Nov 26	Other topics in Digital Logic	
W 28	"	
F 30	"	
M 3	"	
W 5	"	
F 7	"	

Final Exam: Wednesday, December 12, 12-1:50 p.m.

This schedule and work are subject to modifications as announced in class or online.

The Class Wiki is available on the web at: http://fweb.wallawalla.edu/class-wiki/.

Whenever you are absent from class, you are responsible to obtain information that you have missed, understand it. Class is time to "hang out" with your professor. Don't miss it!

Things to bring to class:

1. Laptop or tablet. A phone with WIFI can also work, but probably not quite as well.
2. Earphones.
3. Pencil and paper.
4. Your thinking cap. :-)

How the class will work:

Before class you should do the online preparation work. The accreditation association says that a quarter hour of college credit must have at least an hour in class with the instructor and two hours outside of class of learning engagement. The online prep work Is very good material from a consortium of professors at MIT. There are a variety of ways to learn the material that fit just about any learning style.

When you come to class, you can ask questions if you have some. (You can also come see me in my office.) The main class activity will be working with other students at your table to solve some problems.

There are four assignments online, and two exams. There may be a project after Thanksgiving.

The learning objectives of this class are:

1. Understand about information
2. Why use digital representation of information
3. Understand timing and CMOS
4. Understand combinational logic
5. Understand sequential logic
6. Understand finite state machines
7. Understand optimizing performance and tradeoffs

Instructor information: Rob Frohne, CSP261, (509) 527-2075, rob.frohne@wallawalla.edu, office hours by the office door.

WWU Core themes: Excellence in Thought – Logical Concepts
Beauty in Expression – Design Digital Logic Circuits

If you need assistance because of any physical or learning disability, please contact me or the Special Services offices at 527-2366.

WWU Academic Integrity Policy, Emergency Procedures, Title IX

Last modified: 09/24/18.